

WEST[Help](#)[Logout](#)[Interrupt](#)[Main Menu](#)[Search Form](#)[Posting Counts](#)[Show S Numbers](#)[Edit S Numbers](#)[Preferences](#)[Cases](#)**Search Results -**

Term	Documents
LATCHING.USPT.	72641
LATCHINGS.USPT.	54
DISPLAY.USPT.	359160
DISPLAYS.USPT.	150080
DATA.USPT.	746878
DATUM.USPT.	13732
CLOCK.USPT.	197060
CLOCKS.USPT.	38261
SIGNAL.USPT.	838675
SIGNALS.USPT.	658947
(2 AND ((LATCHING ADJ DISPLAY ADJ DATA) SAME (CLOCK ADJ SIGNAL))).USPT.	6
(L2 AND LATCHING ADJ DISPLAY ADJ DATA SAME CLOCK ADJ SIGNAL).USPT.	6

Database:

US Patents Full-Text Database
US Pre-Grant Publication Full-Text Database
JPO Abstracts Database
EPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Search:

L4

[Refine Search](#)[Recall Text](#)[Clear](#)**Search History**DATE: Thursday, February 27, 2003 [Printable Copy](#) [Create Case](#)

Set Name Query

side by side

DB=USPT; PLUR=YES; OP=ADJ

L4 L2 and latching adj display adj data same clock adj signal
L3 L2 and latching adj display adj data
L2 L1 and clock adj signal
L1 (lcd or liquid adj crystal adj display)

Hit Count Set Name

result set

6 L4
28 L3
5790 L2
63871 L1

END OF SEARCH HISTORY

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	BRS	L1	44858	liquid adj crystal adj display	USPA T	2003/02/2 7 08:08	
2	BRS	L3	2	l2 and multiple adj lines	USPA T	2003/02/2 7 08:08	
3	BRS	L2	524	l1 and gate adj driver\$1	USPA T	2003/02/2 7 08:34	
4	BRS	L4	1662	l1 and bus adj line\$1	USPA T	2003/02/2 7 08:36	
5	BRS	L5	7	l4 and divided and plural adj lines	USPA T	2003/02/2 7 08:39	
6	BRS	L6	47689	l1 bus adj lines and divided	USPA T	2003/02/2 7 08:40	
7	BRS	L7	443	l1 and bus adj lines and divided	USPA T	2003/02/2 7 08:40	
8	BRS	L8	30	l7 and clock adj lines and divided	USPA T	2003/02/2 7 08:40	

WEST**Freeform Search****Database:**

US Patents Full-Text Database
 US Pre-Grant Publication Full-Text Database
 JPO Abstracts Database
 EPO Abstracts Database
 Derwent World Patents Index
 IBM Technical Disclosure Bulletins

Term:
Display: **Documents in Display Format:** **Starting with Number**
Generate: ☐ Hit List ☒ Hit Count ☐ Side by Side ☐ Image

Search

Clear

Help

Logout

Interrupt

Main Menu

Show S Numbers

Edit S Numbers

Preferences

Cases

Search History
DATE: Thursday, February 27, 2003 [Printable Copy](#) [Create Case](#)
Set Name Query

side by side

Hit Count Set Name

result set

DB=USPT; PLUR=YES; OP=ADJ

<u>L8</u>	L7 and divided adj bus adj line\$1	7	<u>L8</u>
<u>L7</u>	bus adj line\$1	14544	<u>L7</u>
<u>L6</u>	L1 and divided adj clock adj line\$1	1	<u>L6</u>
<u>L5</u>	L4 and divided adj clock adj line\$1	1	<u>L5</u>
<u>L4</u>	l1 and clock adj line\$1	675	<u>L4</u>
<u>L3</u>	L1 and divided adj bus adj lines	2	<u>L3</u>
<u>L2</u>	L1 and divided adj bus lines	2	<u>L2</u>
<u>L1</u>	(LCD or liquid adj crystal adj display)	63871	<u>L1</u>

END OF SEARCH HISTORY

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	BRS	L1	44858	liquid adj crystal adj display	USPA T	2003/02/2 6 12:47	
2	BRS	L2	5115	l1 and circuit adj board\$1	USPA T	2003/02/2 6 12:48	
3	BRS	L3	440	l2 and drive adj circuit\$1	USPA T	2003/02/2 6 12:48	
4	BRS	L5	42	l4 and connect\$2	USPA T	2003/02/2 6 12:49	
5	BRS	L4	42	l3 and bus adj line\$1	USPA T	2003/02/2 6 12:50	
6	BRS	L6	68394	printed adj circuit adj board\$1	USPA T	2003/02/2 6 13:03	
7	BRS	L7	890	l6 and bus adj lines	USPA T	2003/02/2 6 13:04	
8	BRS	L8	0	l7 and bus adj lines adj on adj printed adj circuit adj board\$1	USPA T	2003/02/2 6 13:05	
9	BRS	L9	89	l7 and bus adj lines same printed adj circuit adj board\$1	USPA T	2003/02/2 6 13:06	
10	BRS	L10	24	l9 and display\$1	USPA T	2003/02/2 6 13:06	

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	IS&R	L1	0	("display").PN.	USPA T	2003/02/26 10:40	
2	BRS	L2	35916 0	display	USPA T	2003/02/26 10:40	
3	BRS	L3	24132	l2 and circuit adj board\$1	USPA T	2003/02/26 10:41	
4	BRS	L4	6177	l3 and liquid adj crystal	USPA T	2003/02/26 10:42	
5	BRS	L5	543	l4 and drive adj circuit\$1	USPA T	2003/02/26 10:43	
6	BRS	L6	21813	l5 an display adj control	USPA T	2003/02/26 10:44	
7	BRS	L7	457	l4 and display adj control	USPA T	2003/02/26 10:45	
8	BRS	L8	60	l7 and drive adj circuit\$1	USPA T	2003/02/26 10:45	
9	BRS	L9	4	l8 and bus adj line\$1	USPA T	2003/02/26 10:46	